

AMENDMENT TO CLAIMS:

1. (Currently Amended) An electroluminescence display device comprising:  
a display pixel region disposed on a substrate and comprising an electroluminescence element having, an electroluminescence element including an emissive layer between first and second electrodes, an emissive layer including an organic compound; and  
a peripheral drive circuit region having a peripheral drive circuit that is integrated disposed on said substrate, for controlling said electroluminescence element located in said display pixel region, said peripheral drive circuit being integrated in a peripheral region on an outside of said display pixel region and between said display pixel region and edges of said substrate, said peripheral drive circuit having a plurality of thin film transistors said drive circuit region having thin film transistors for driving said electroluminescence element; wherein  
said second electrode of said electroluminescence element is a discrete anode;  
said emissive layer is formed overlapping said second electrode;  
said first electrode is formed overlapping said emissive layer as a common electrode for entirely overlaps said display pixel region, said first electrode disposed overlapping said display pixel region, terminating in a substrate region on an inside with respect to said peripheral region, and being absent from said peripheral region and is absent from at least said drive circuit region, said first electrode is a common cathode, and said second electrode is a discrete anode.
2. (Canceled)
3. (Currently Amended) The device defined in Claim 1 wherein:  
said display pixel region includes first and second thin film transistors for driving said electroluminescence element;  
an insulating film is formed overlapping said first and second thin film transistors and said thin film transistors of said peripheral drive circuit region; and  
said first electrode is formed over said insulating film in a position opposing said display pixel region.
4. (Currently Amended) The device defined in Claim 3 wherein a circuit in said peripheral drive circuit region includes a CMOS connection structure in which a p-type channel thin film transistor and a n-type channel thin film transistor are complementarily connected.
5. (Currently Amended) The device defined in Claim 1 wherein:

said thin film transistors of said peripheral drive circuit region are bottom gate type transistors having gate electrodes located beneath an active layer; and

said first electrode is formed over an insulating layer extending on the entire substrate on an opposite side of said active layer from which said gate electrodes are located, said first electrode formed overlapping said display pixel region.

6. (Cancelled)

7. (Canceled)

8. (Currently Amended) An electroluminescence display device comprising a substrate provided with:

a display pixel region having an electroluminescence element including an emissive layer between an anode and a cathode, and first and second thin film transistors for driving said electroluminescence element, said emissive layer includes an organic compound; and

a peripheral drive circuit region having a peripheral drive circuit that is integrated on said substrate in a peripheral region on an outside of said display pixel region and between said display pixel region and edges of said substrate, said peripheral drive circuit region having third thin film transistors for driving said first and second thin film transistors; wherein

said anode is formed overlapping said emissive layer,

said cathode is disposed in said display pixel region and is absent from said drive circuit regions  
said cathode overlaps said display pixel region, terminates in a substrate region on an inside of said peripheral region, and is absent from said peripheral region.

9. (Currently Amended) The device defined in Claim 8 wherein said cathode on said substrate is formed over the entire display pixel region as a common electrode, and is absent from at least said drive circuit region.

10. (Currently Amended) The device defined in Claim 8 wherein a circuit in said peripheral drive circuit region includes a CMOS connection structure in which a p-type channel thin film transistor and a n-type channel thin film transistor are complementarily connected.

11. (Currently Amended) The device defined in Claim 8 wherein:

said third thin film transistors of said peripheral drive circuit region are bottom gate type transistors having gate electrodes located beneath an active layer; and

said cathode is formed over an insulating layer extending on the entire substrate on an opposite side of said active layer from which said gate electrodes are located, ~~said cathode formed overlapping said display pixel region.~~

12. (Currently Amended) An emissive display device comprising:

a display pixel region disposed on a substrate and having an emissive element including an emissive layer between first and second electrodes, said emissive layer includes an organic compound; and

a peripheral drive circuit region having a peripheral drive circuit that is integrated on said substrate in a peripheral region on an outside of said display pixel region and between said display pixel region and edges of said substrate, said peripheral drive circuit region having thin film transistors for driving said emissive element;

wherein said first electrode overlaps said display pixel region, terminates in a substrate region on an inside of said peripheral region, and is absent from said peripheral region overlaps the entire display pixel region and is absent from at least said drive circuit region, said first electrode is a common cathode, and said second electrode is formed overlapping said emissive layer, said second electrode is a discrete anode.

13. (Currently Amended) An electroluminescence display device comprising a substrate provided with:

a display pixel region having an electroluminescence element including an emissive layer between an anode and a cathode, and first and second thin film transistors for driving said electroluminescence element, said cathode is formed in a layer extending above a layer in which said anode is formed, said emissive layer includes an organic compound; and

a peripheral drive circuit region having a peripheral drive circuit that is integrated on said substrate in a peripheral region on an outside of said display pixel region and between said display pixel region and edges of said substrate, said peripheral drive circuit region having third thin film transistors for driving said first and second thin film transistors; wherein

said anode is formed overlapping said emissive layer,

said cathode said cathode overlaps said display pixel region, terminates in a substrate region on an inside of said peripheral region, and is absent from said peripheral region is disposed in said display pixel region and is absent from said drive circuit region.

14. (Currently Amended) The device as defined in Claim 1, wherein said cathode includes an end portion that extends to an area between said display pixel region and said peripheral drive circuit-region.

15. (Currently Amended) The device as defined in Claim 8, wherein said cathode includes an end portion that extends to an area between said display pixel region and said peripheral drive circuit-region.

16. (Currently Amended) The device as defined in Claim 12, wherein said cathode includes an end portion that extends to an area between said display pixel region and said peripheral drive circuit-region.

17. (Currently Amended) The device as defined in Claim 13, wherein said cathode includes an end portion that extends to an area between said display pixel region and said peripheral drive circuit-region.

18. (Currently Amended) An electroluminescence display device comprising:

a display pixel region disposed on a substrate and having an electroluminescence element including an emissive layer between first and second electrodes, said emissive layer includes an organic compound; and

a peripheral drive circuit region integrated on said substrate in a peripheral region on an outside of said display pixel region and between said display pixel region and edges of said substrate, said peripheral drive circuit region having vertical and horizontal drive circuits that are located in a peripheral drive circuit region formed surrounding said display pixel region and are integrated on said substrate, said peripheral drive circuit region having thin film transistors for driving said electroluminescence element; wherein

said first electrode overlaps said display pixel region, terminates in a substrate region on an inside of said peripheral drive circuit region, and is absent from said peripheral drive circuit region entirely overlaps said display pixel region and is absent from at least said drive circuit region,

said first electrode is a common cathode, comprises an opaque metal material, and constitutes an uppermost layer of said electroluminescence element, and

said second electrode is formed overlapping said emissive layer, said second electrode is a discrete anode.

19. (Currently Amended) The device as defined in Claim 18, wherein said cathode includes an end portion that extends to an area between said display pixel region and said peripheral drive circuit region.

20. (Currently Amended) An electroluminescence display device comprising a substrate provided with:

a display pixel region having an electroluminescence element including an emissive layer between an anode and a cathode, and first and second thin film transistors for driving said electroluminescence element, said emissive layer includes an organic compound; and

a peripheral drive circuit region integrated on said substrate in a peripheral region on an outside of said display pixel region and between said display pixel region and edges of said substrate, said peripheral drive circuit region having vertical and horizontal drive circuits that are located in a peripheral drive circuit region formed surrounding said display pixel region and are integrated on said substrate, said peripheral drive circuit region having third thin film transistors for driving said first and second thin film transistors,

wherein said anode is formed overlapping said emissive layer,

said cathode overlaps said display pixel region, terminates in a substrate region on an inside of said peripheral drive circuit region, and is absent from said peripheral drive circuit region is disposed in said display pixel region and is absent from said drive circuit region,

said cathode comprises an opaque metal material and constitutes an uppermost layer of said electroluminescence element.

21. (Currently Amended) The device as defined in Claim 20, wherein said cathode includes an end portion that extends to an area between said display pixel region and said peripheral drive circuit region.

22. (Currently Amended) An emissive display device comprising:

a display pixel region disposed on a substrate and having an emissive element including an emissive layer between first and second electrodes, said emissive layer includes an organic compound; and

a peripheral drive circuit integrated on said substrate in a peripheral region on an outside of said display pixel region and between said display pixel region and edges of said substrate, said peripheral drive circuit having vertical and horizontal drive circuits that are located in a peripheral drive circuit region formed surrounding said display pixel region and are integrated on

said substrate, said peripheral drive circuit region having thin film transistors for driving said emissive element;

wherein said first electrode overlaps said display pixel region, terminates in a substrate region on an inside of said peripheral region, and is absent from said peripheral region~~overlaps the entire display pixel region and is absent from at least said drive circuit region~~, said first electrode is a common cathode, and said second electrode is formed overlapping said emissive layer, said second electrode is a discrete anode,

said cathode comprises an opaque metal material and constitutes an uppermost layer of said electroluminescence element.

23. (Currently Amended) The device as defined in Claim 22, wherein said cathode includes an end portion that extends to an area between said display pixel region and said peripheral drive circuit region.

24. (Currently Amended) An electroluminescence display device comprising a substrate provided with:

a display pixel region having an electroluminescence element including an emissive layer between an anode and a cathode, and first and second thin film transistors for driving said electroluminescence element, said emissive layer includes an organic compound, said cathode is formed in a layer extending above a layer in which said anode is formed; and

a peripheral drive circuit region integrated on said substrate in a peripheral region on an outside of said display pixel region and between said display pixel region and edges of said substrate, said peripheral drive circuit region having vertical and horizontal drive circuits ~~that are located in a peripheral drive circuit region formed surrounding said display pixel region and are integrated on said substrate, said peripheral drive circuit region~~ having third thin film transistors for driving said first and second thin film transistors,

wherein said anode is formed overlapping said emissive layer,

said cathode overlaps said display pixel region, terminates in a substrate region on an inside of said peripheral drive circuit region, and is absent from said peripheral drive circuit region~~is disposed in said display pixel region and is absent from said drive circuit region~~, and

said cathode comprises an opaque metal material and constitutes an uppermost layer of said electroluminescence element.

25. (Currently Amended) The device as defined in Claim 24, wherein said cathode includes an end portion that extends to an area between said display pixel region and said peripheral drive circuit region.

26. (Currently Amended) An emissive display device comprising:

a display pixel region disposed on a substrate and having an emissive element including an emissive layer between first and second electrodes, said emissive layer includes an organic compound; and

a peripheral drive circuit region disposed on the sameaid substrate and is formed in a region aroundsurrounding said display pixel region and betweensaid display pixel region and edges of said substrate, said peripheral drive circuit region having thin film transistors for driving said emissive element;

wherein said first electrode overlaps the entire display pixel region, terminates in a substrate region on an inside of said peripheral drive circuit region, and is absent from at least said peripheral drive circuit region, said first electrode is a common cathode, and said second electrode is a discrete anode, and

said cathode includes an end portion that extends to an area between said display pixel region and said peripheral drive circuit region.